Dr. Hans Cho joined the U.S. Naval Research Laboratory (NRL) in 2016 as a Materials Research Staff Scientist. Prior to joining NRL, he has worked mainly in corporate research: developing techniques for growing single-crystal regimes in semiconductor thin films on insulator using pulsed laser crystallization and UHV-CVD epitaxy at the Samsung Advanced Institute of Technology (SAIT) in Korea (2003-2006), and novel nanofabrication including CVD growth of Si and Ge nanostructures at HP(E) Labs in Palo Alto, CA (2007-2016). He joined HP's memristor development effort in 2010, and for six years was engaged in projects to commercialize memristors for memory chip technology, including ~two years (2010-2012) stationed in Korea as the tech transfer liaison to Hynix Semiconductor (now SK Hynix) and six months working with SanDisk (Western Digital). He is currently heading an effort at NRL to develop neuromorphic hardware elements based on memristors, and is NRL's technical lead for the OSD-funded Tri-Service Advanced Research for the Advancement of S&T Priorities (ARAP) program titled "Combined Development Pipeline for Novel Neuromorphic Hardware (NeuroPipe)". He received a BS in Physics at the Massachusetts Institute of Technology in 1995, and an MS and DES in Applied Physics and Mathematics (Program in Materials) from Columbia University in 2003.